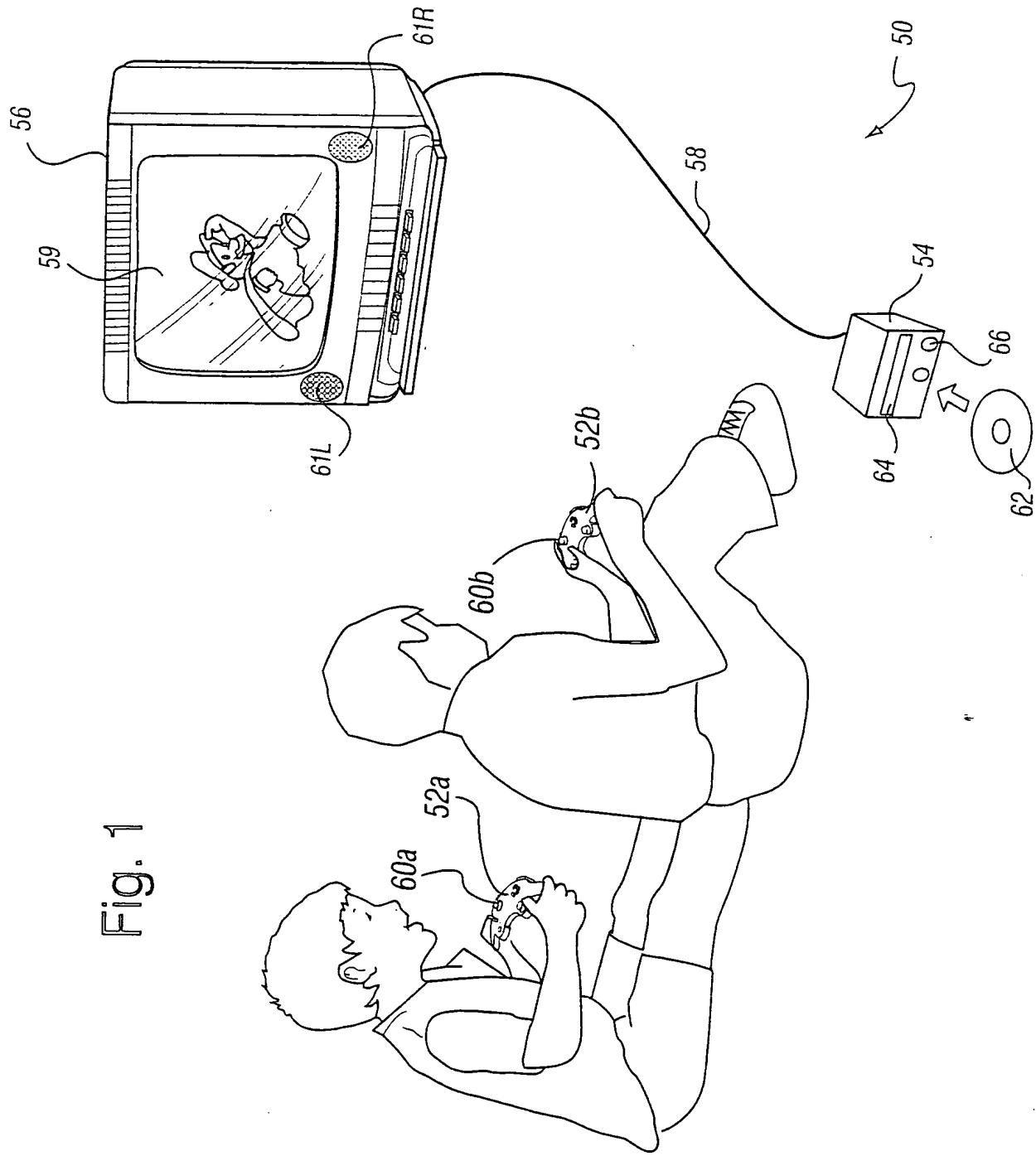


Fig. 1



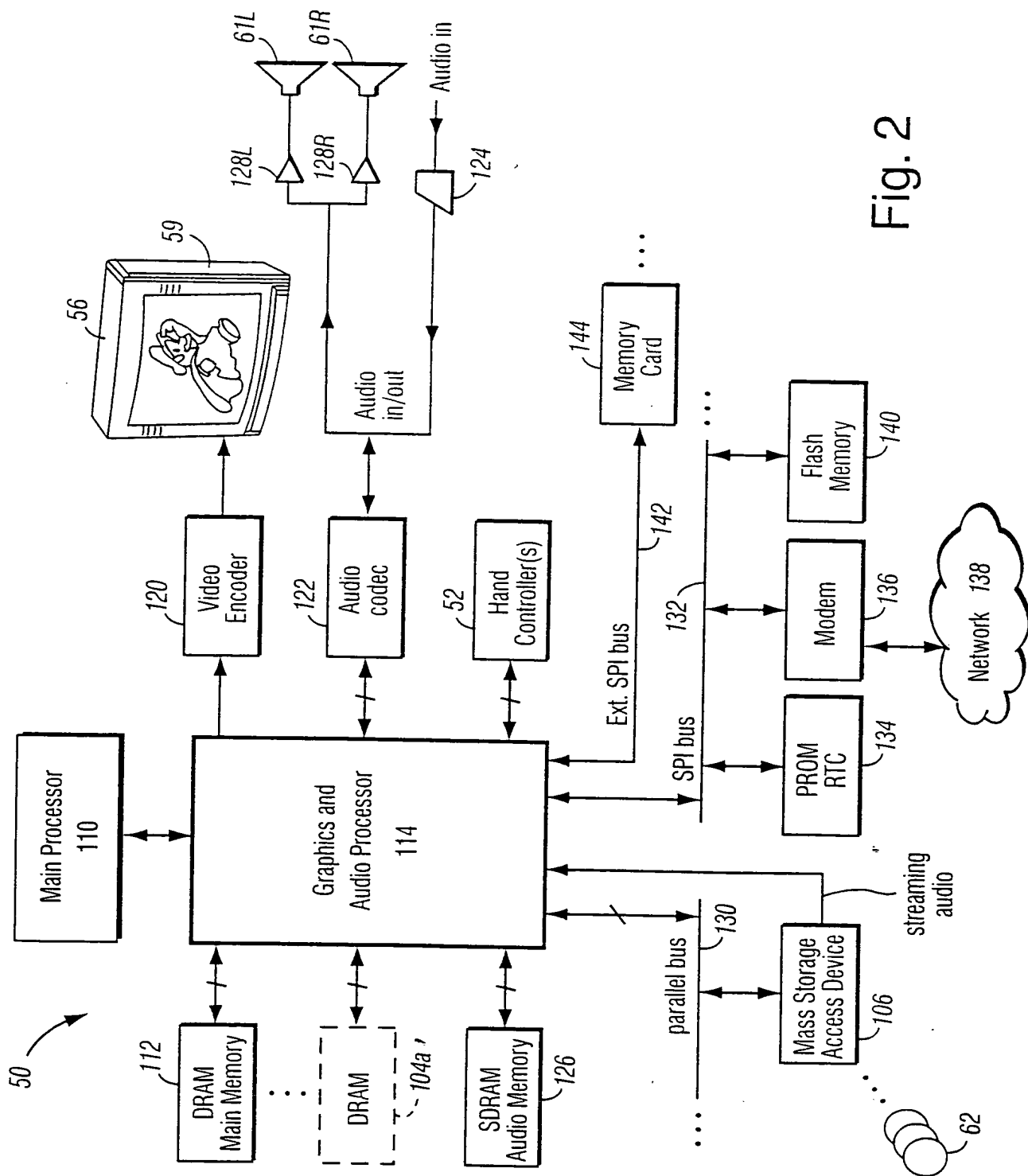
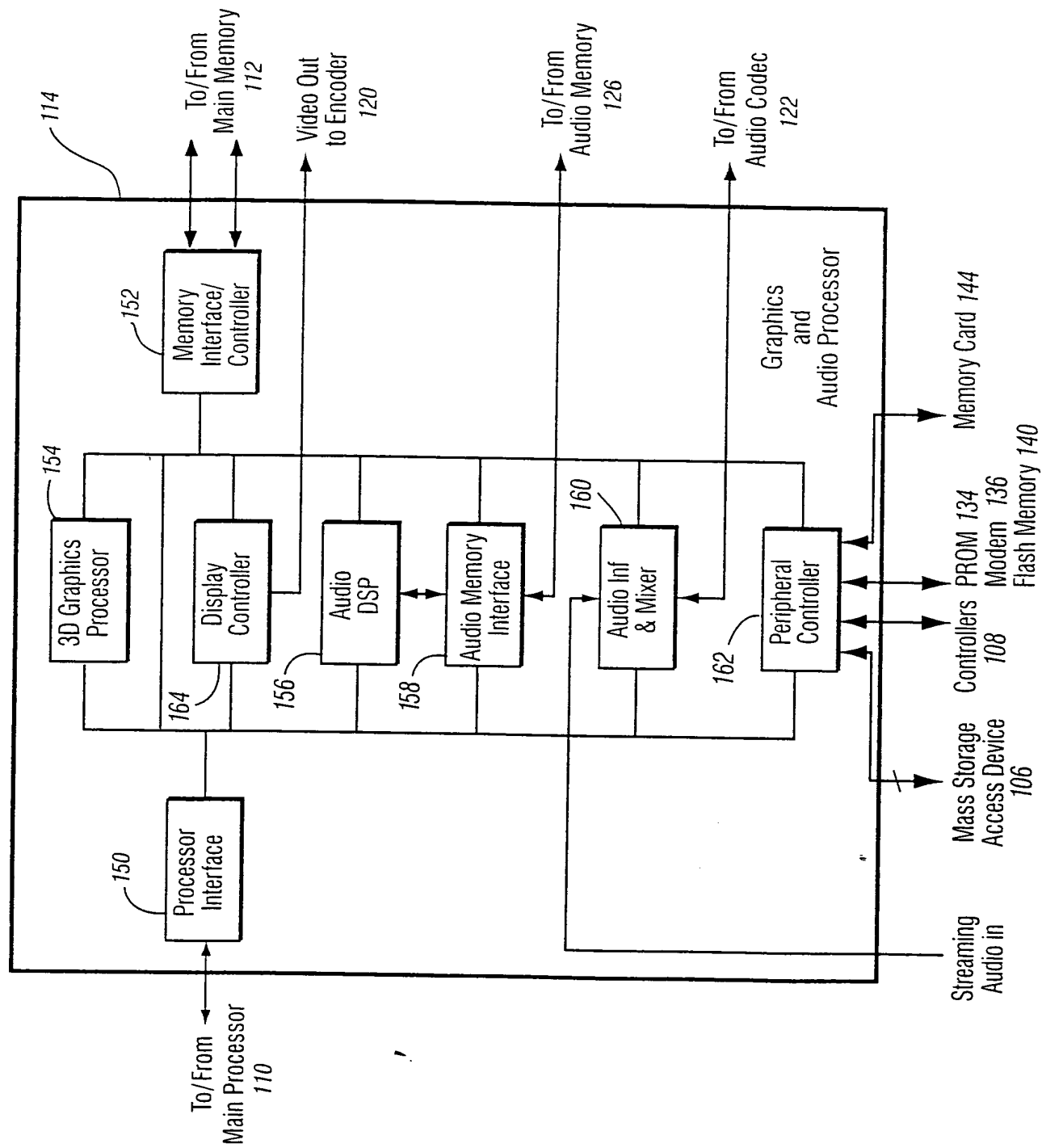


Fig. 2

Fig. 3



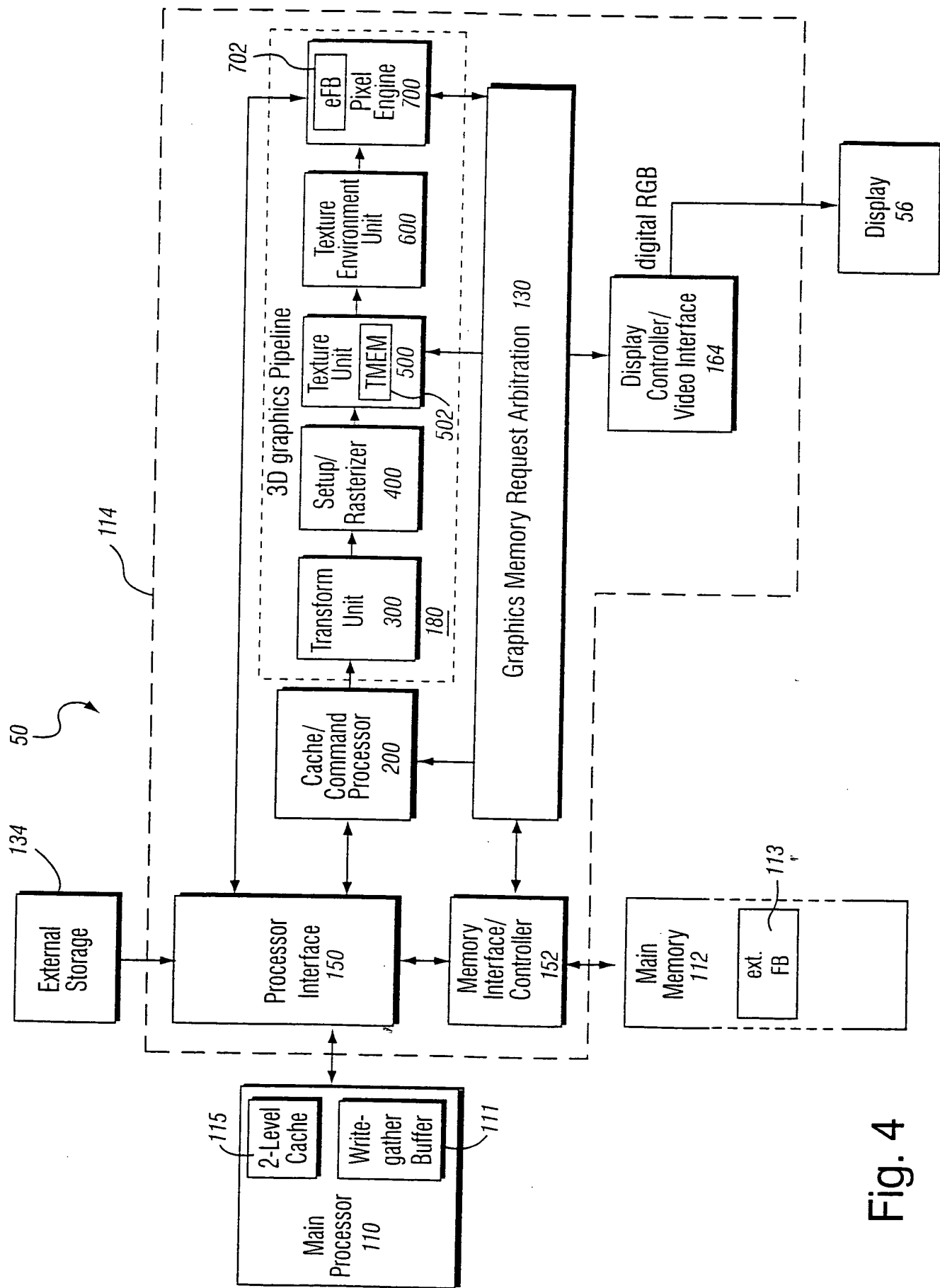


Fig. 4

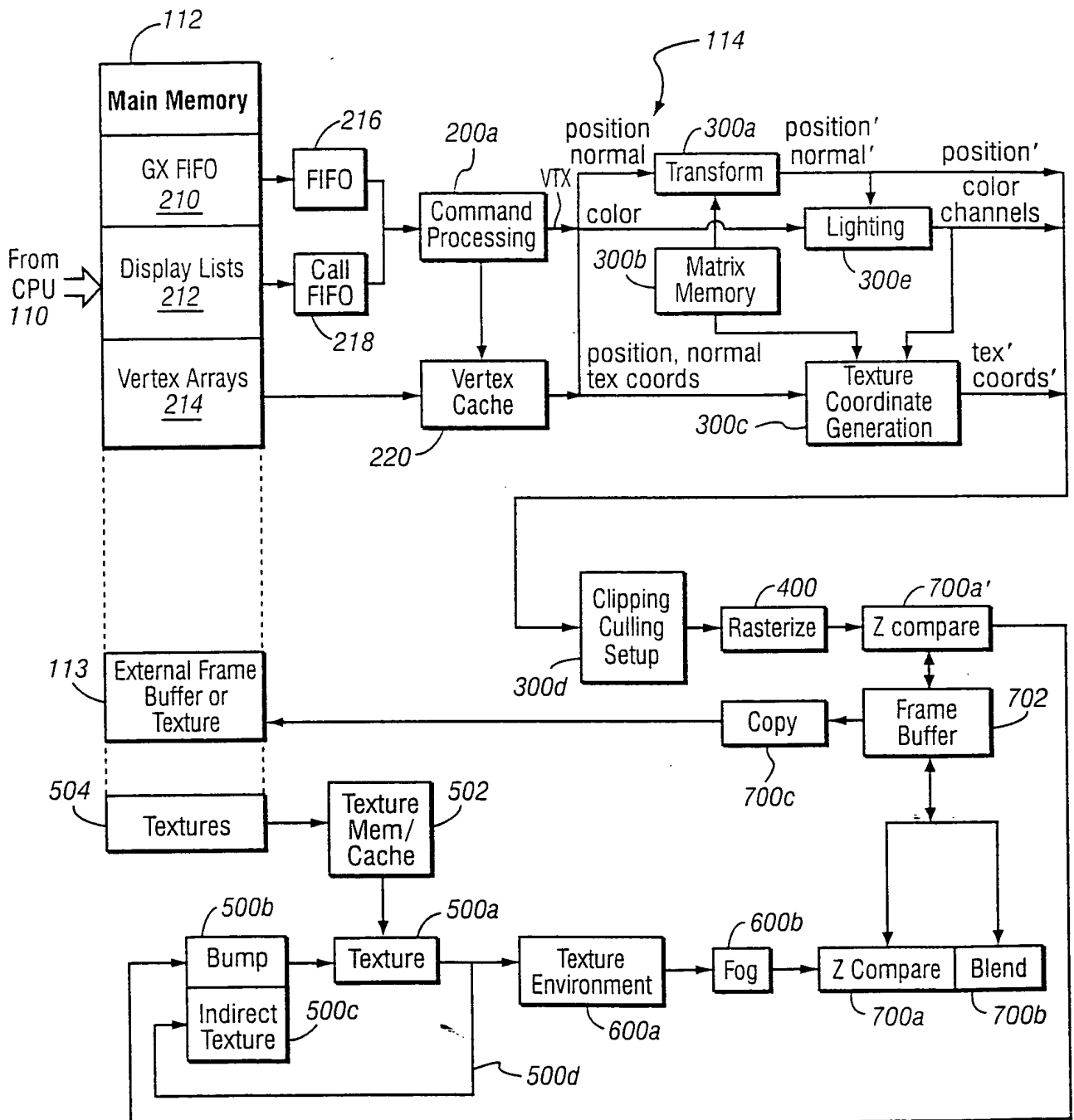


Fig. 5 EXAMPLE GRAPHICS PROCESSOR FLOW

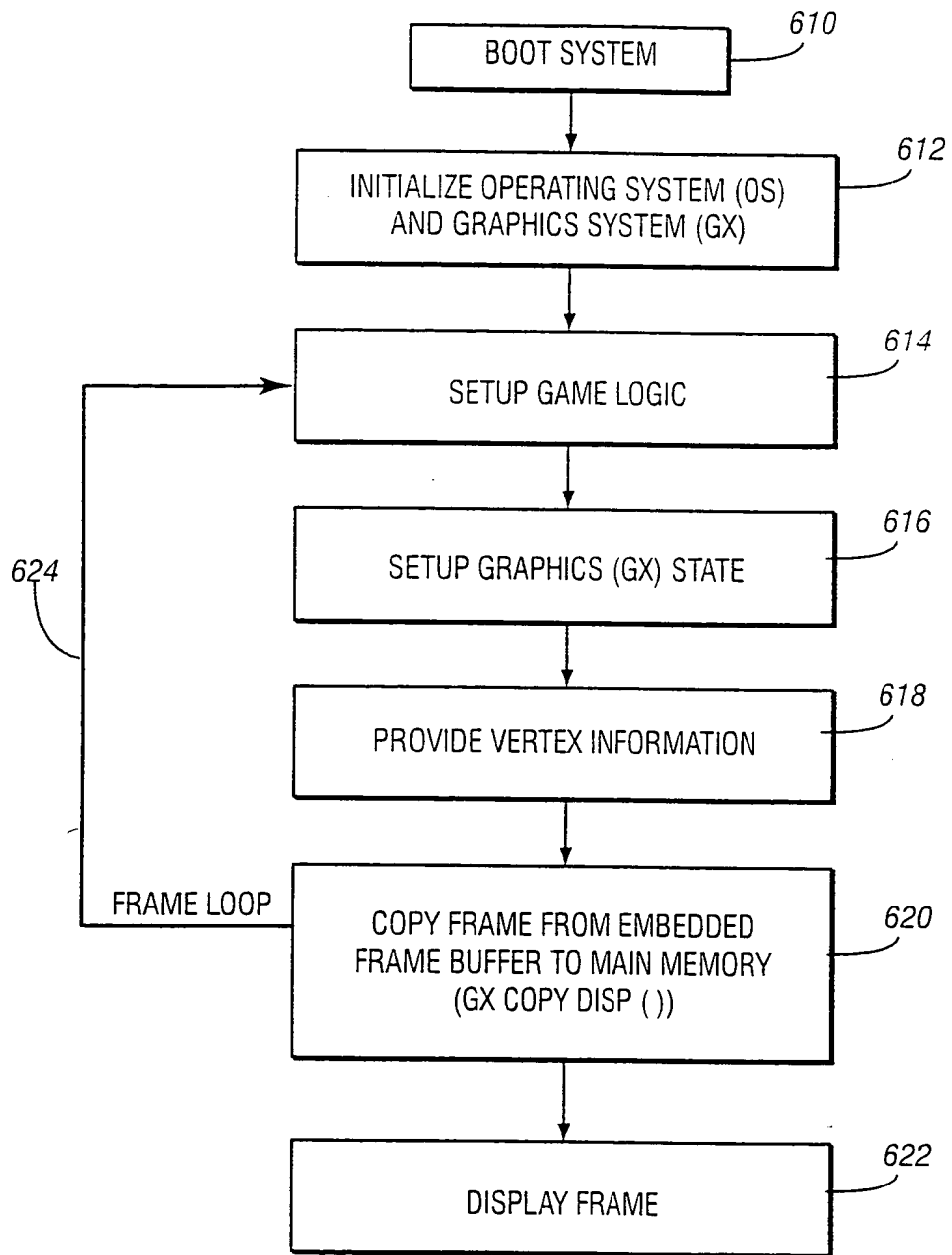
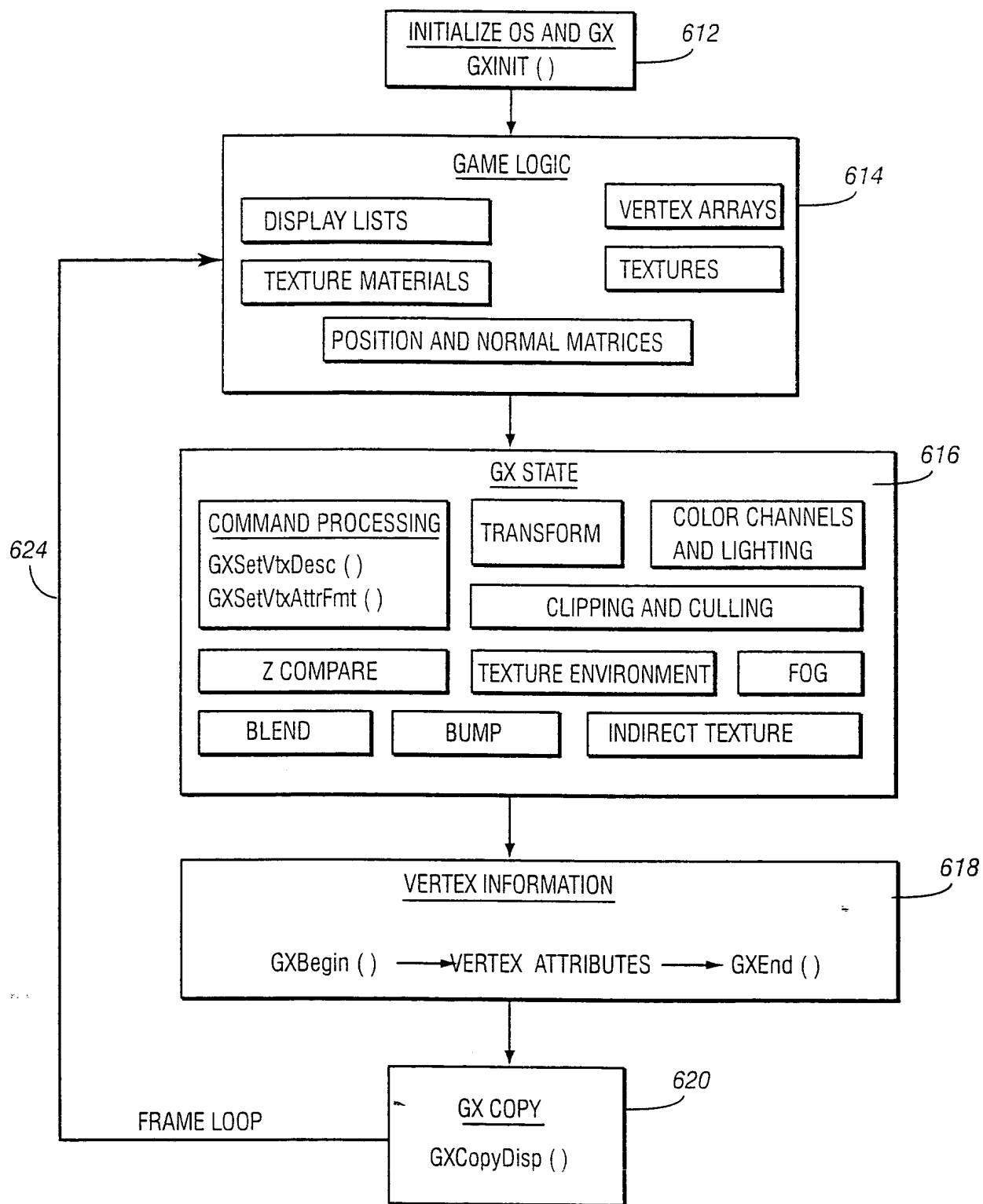


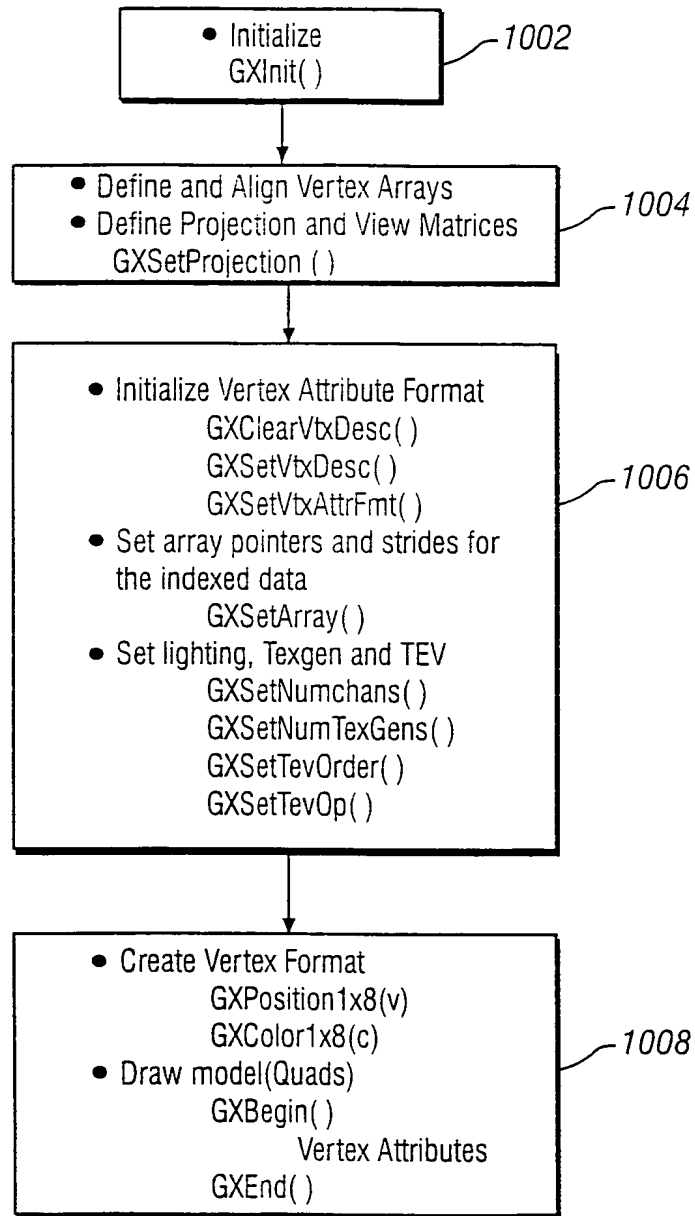
Fig. 6

EXAMPLE GRAPHICS
PROCESSING LOOP



EXAMPLE MORE DETAILED GRAPHIC
PROCESSING LOOP

Fig. 7



EXAMPLE SIMPLE GRAPHIC
APPLICATION

Fig. 8

EXAMPLE GX SET COPY CLEAR (BLACK,0x00FFFFFF):

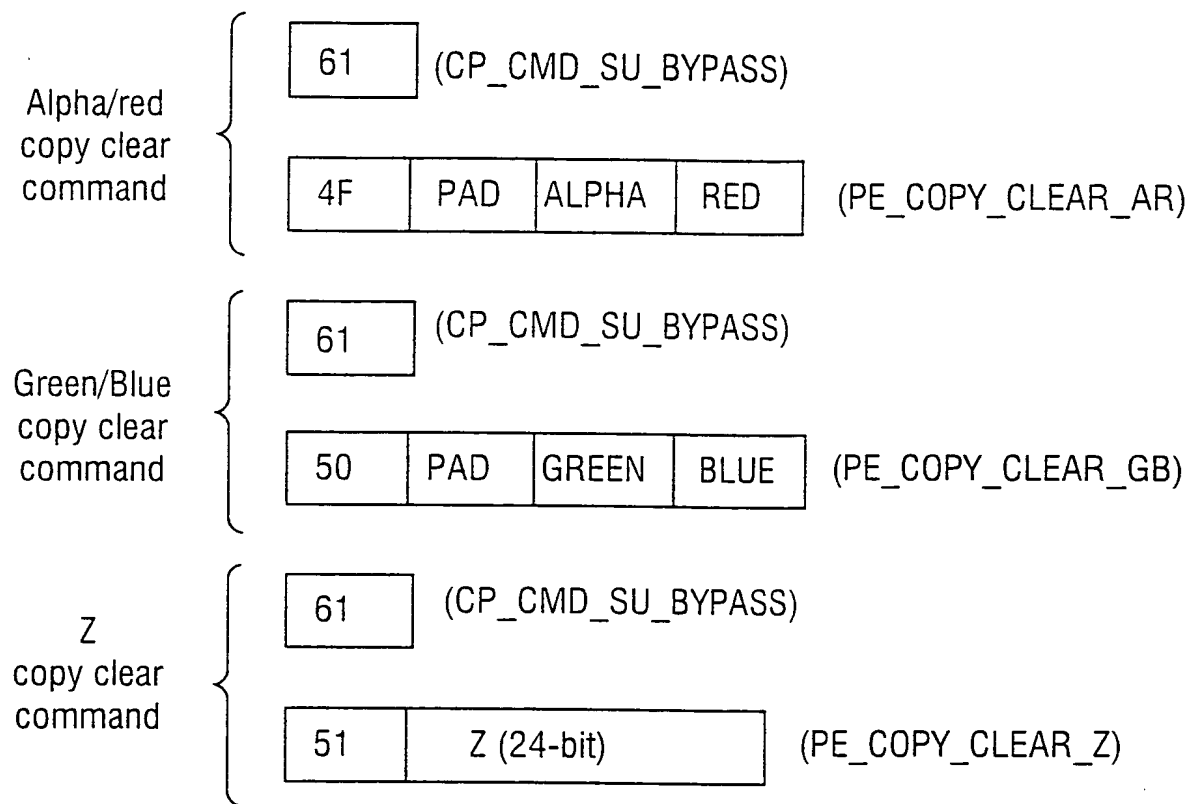


Fig. 9

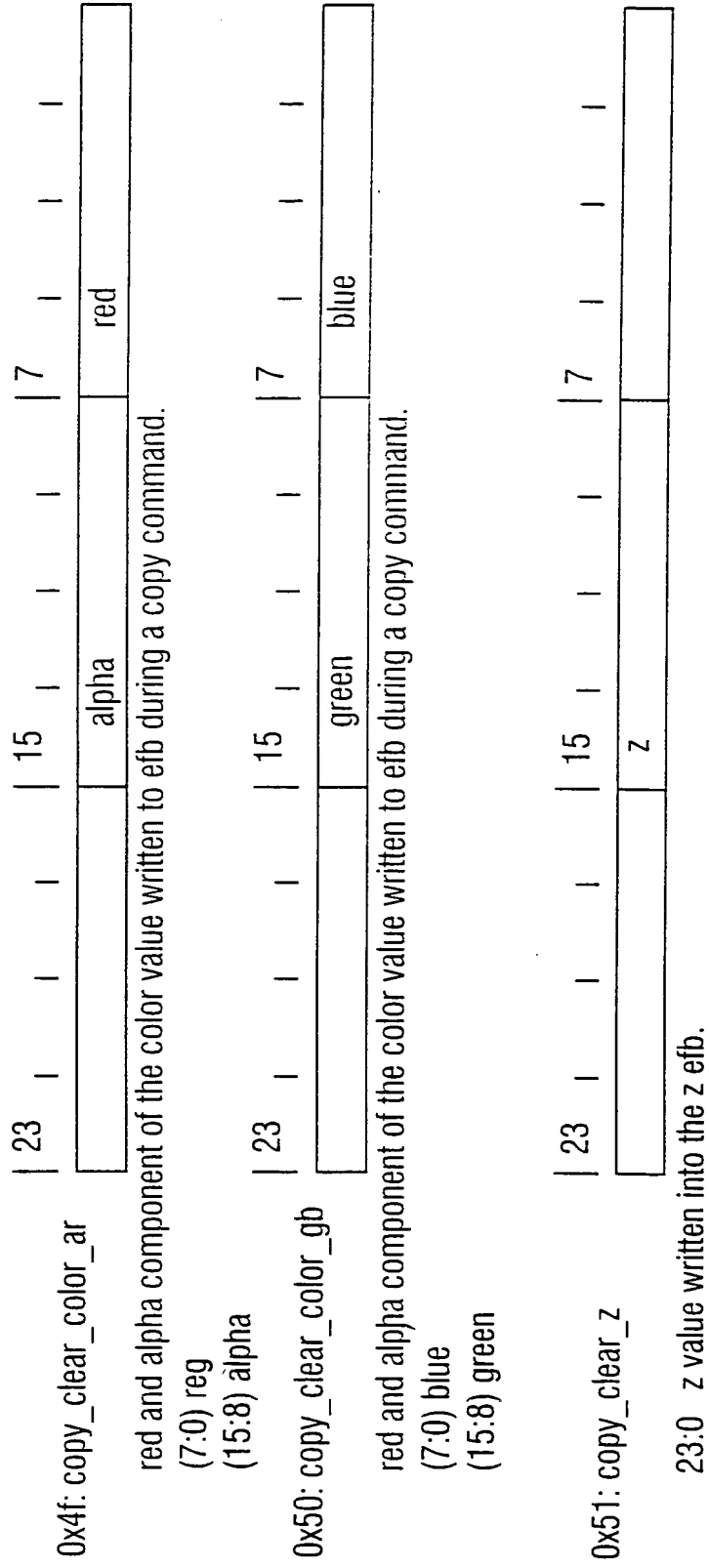


Fig. 9a

EXAMPLE PIXEL ENGINE COPY CLEAR
REGISTER FORMATS

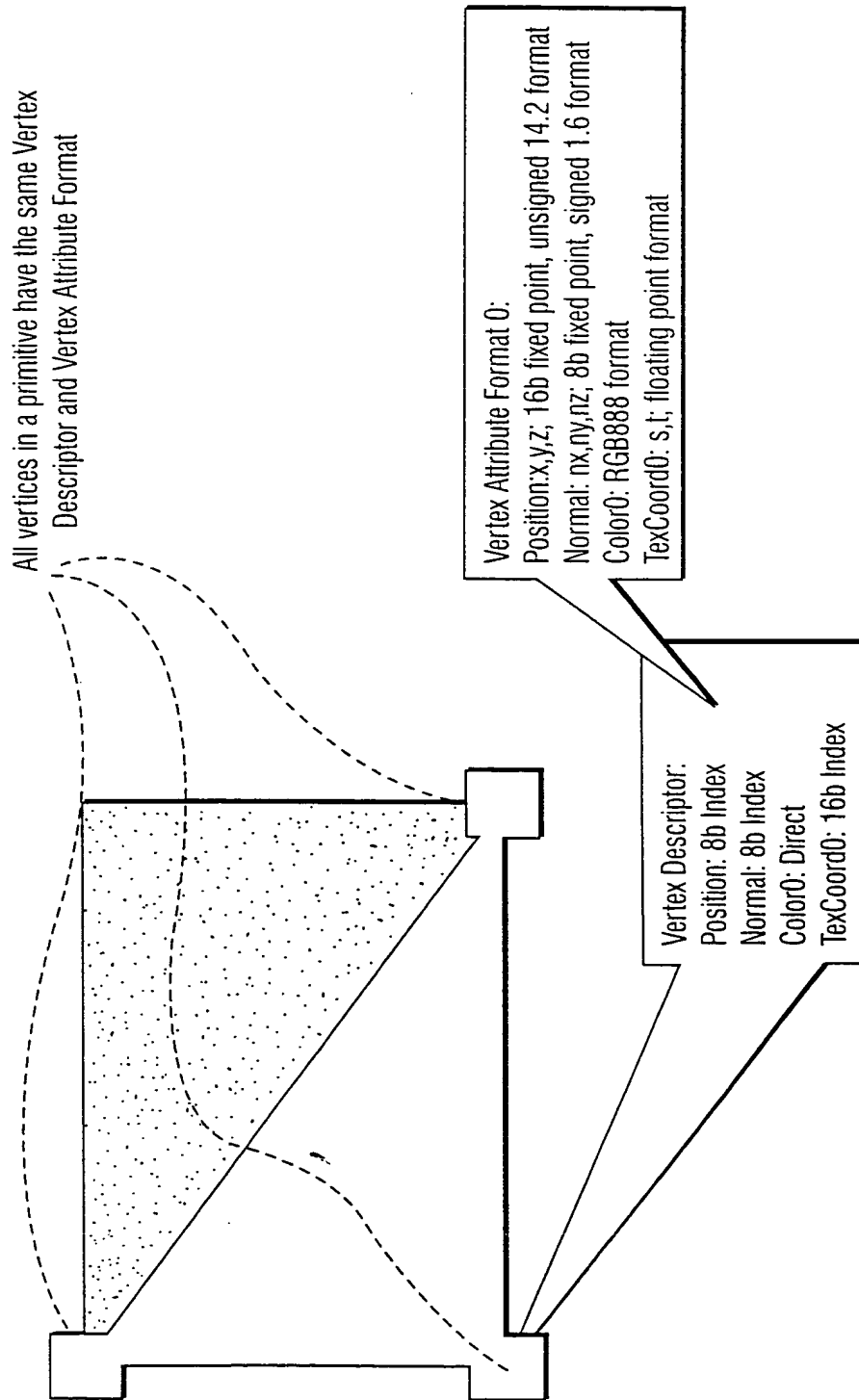


Fig. 10 EXAMPLE (VERTEX AND ATTRIBUTE DESCRIPTION)

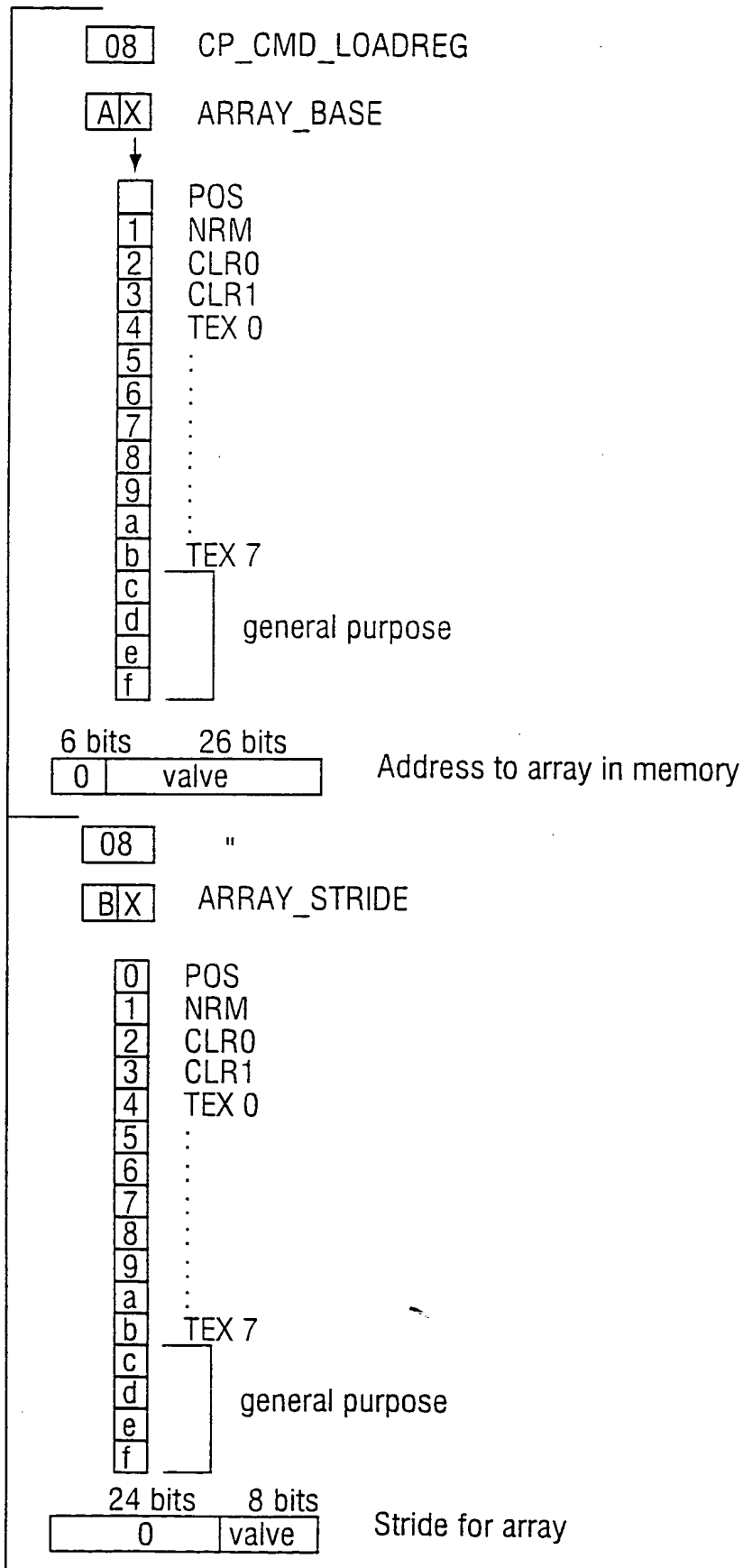


Fig. 11 GX SET ARRAY (GX ATTR, ATTR, VOID + BASE APTR, .8 STRIDE)

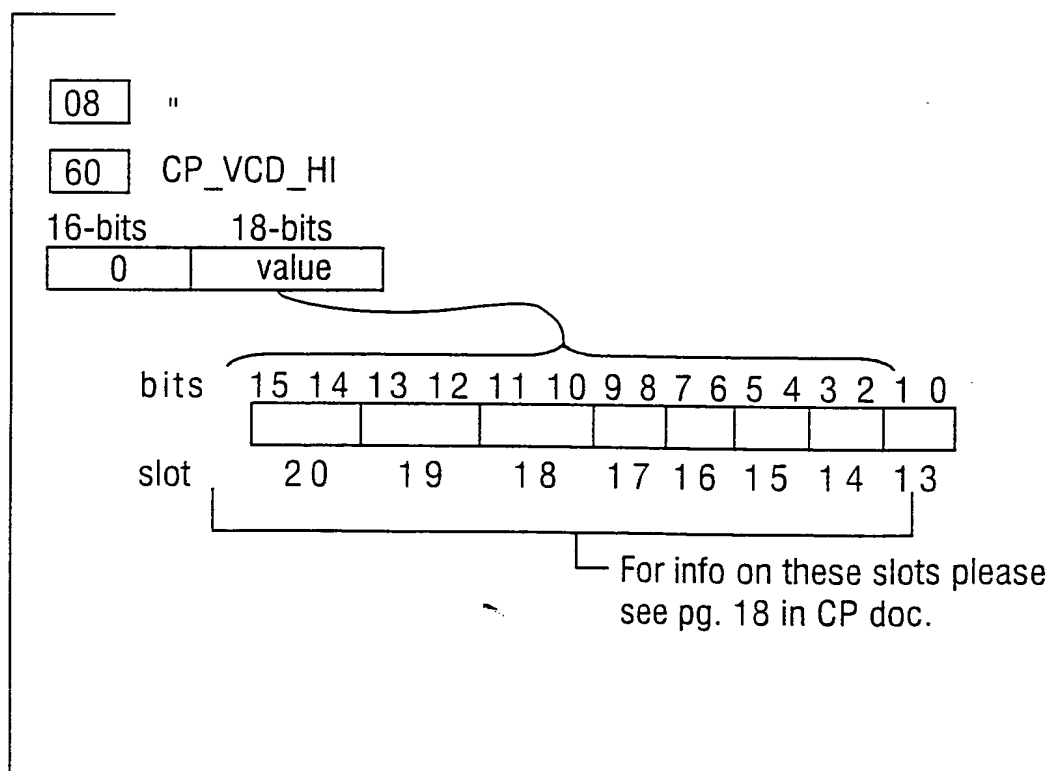
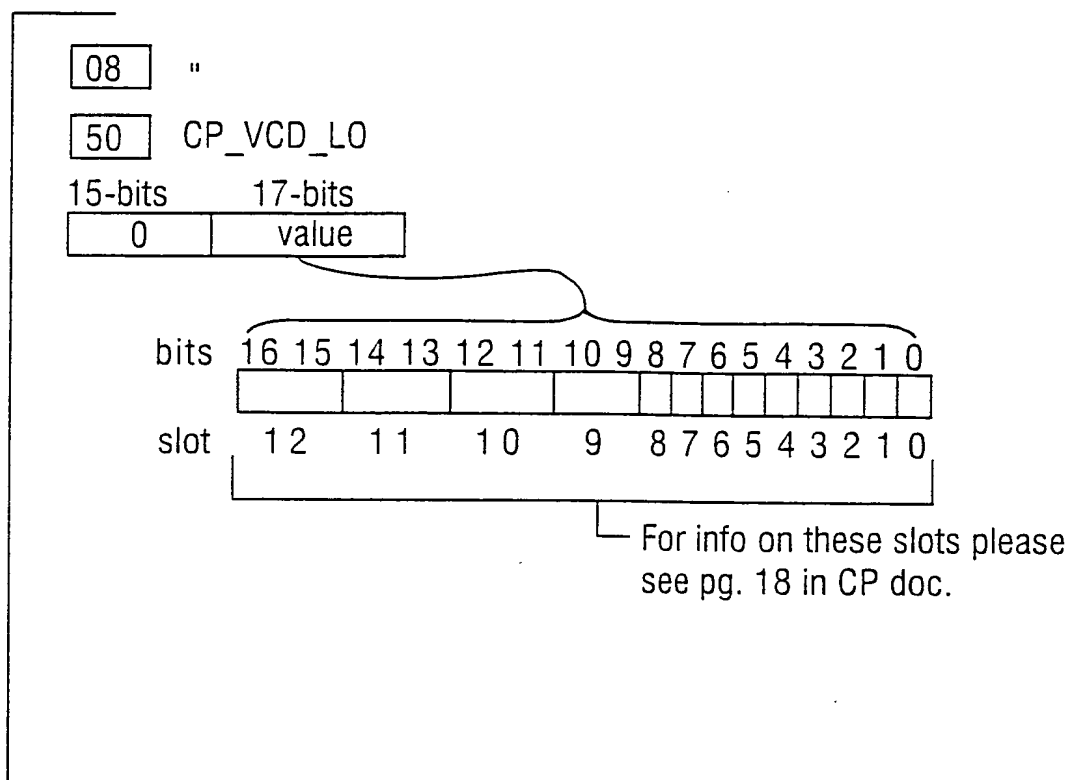


Fig. 12

GX SET VTX ATTR ATTR

GX SET VTX ATTR FMT();

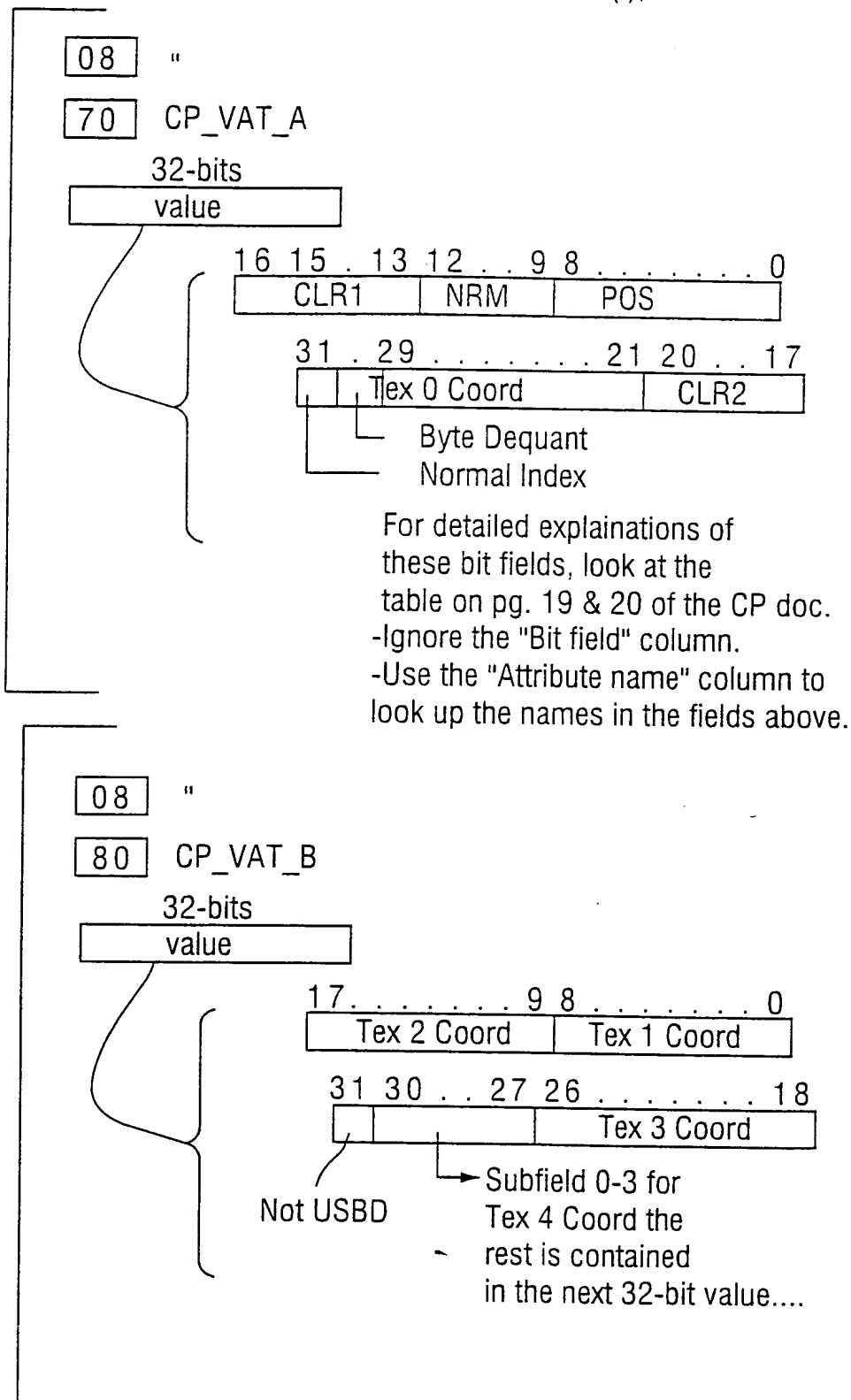


Fig. 13A

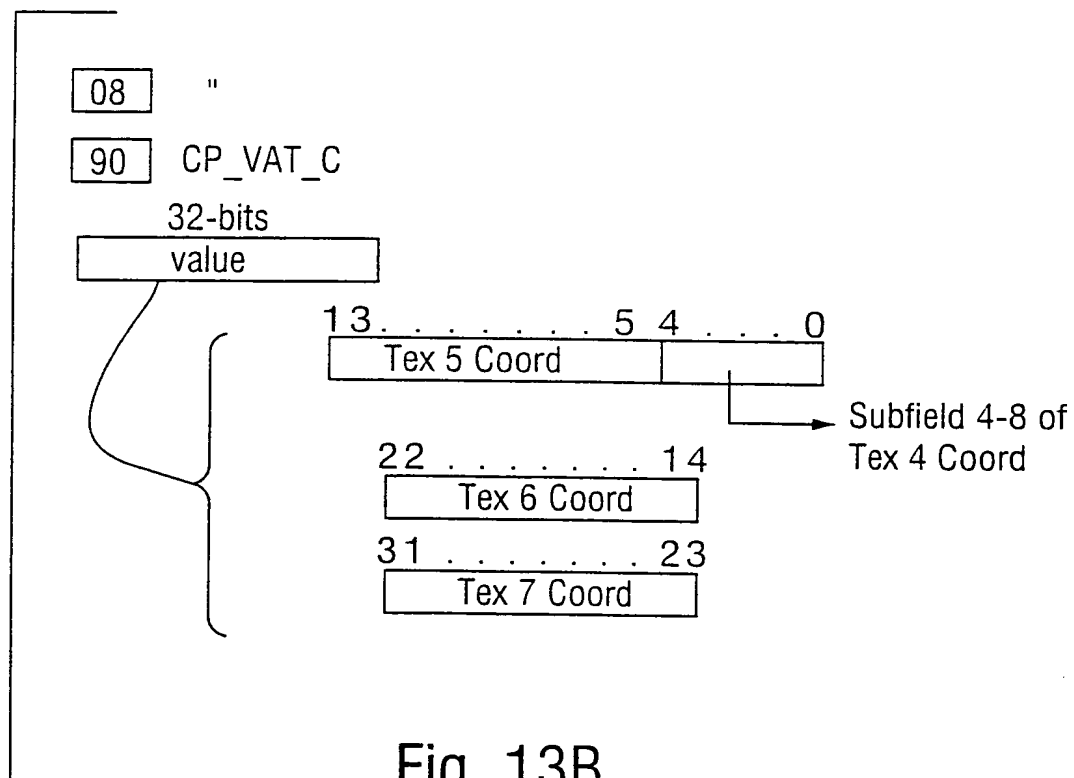


Fig. 13B

	GX_VTXFMT0	GX_VTXFMT1	GX_VTXFMT2	GX_VTXFMT3	GX_VTXFMT4	GX_VTXFMT5	GX_VTXFMT6	GX_VTXFMT7
GX_VA_POS	n elements format/size scale							
GX_VA_NRM				format/size				
GX_VA_CLR0							n elements format/size	
GX_VA_CLR1								
GX_VA_TEX0	n elements format/size scale							
GX_VA_TEX1								
GX_VA_TEX2								
GX_VA_TEX3								
GX_VA_TEX4								
GX_VA_TEX5								
GX_VA_TEX6								
GX_VA_TEX7								

Fig. 14 (VERTEX ATTRIBUTE FORMAT TABLE)

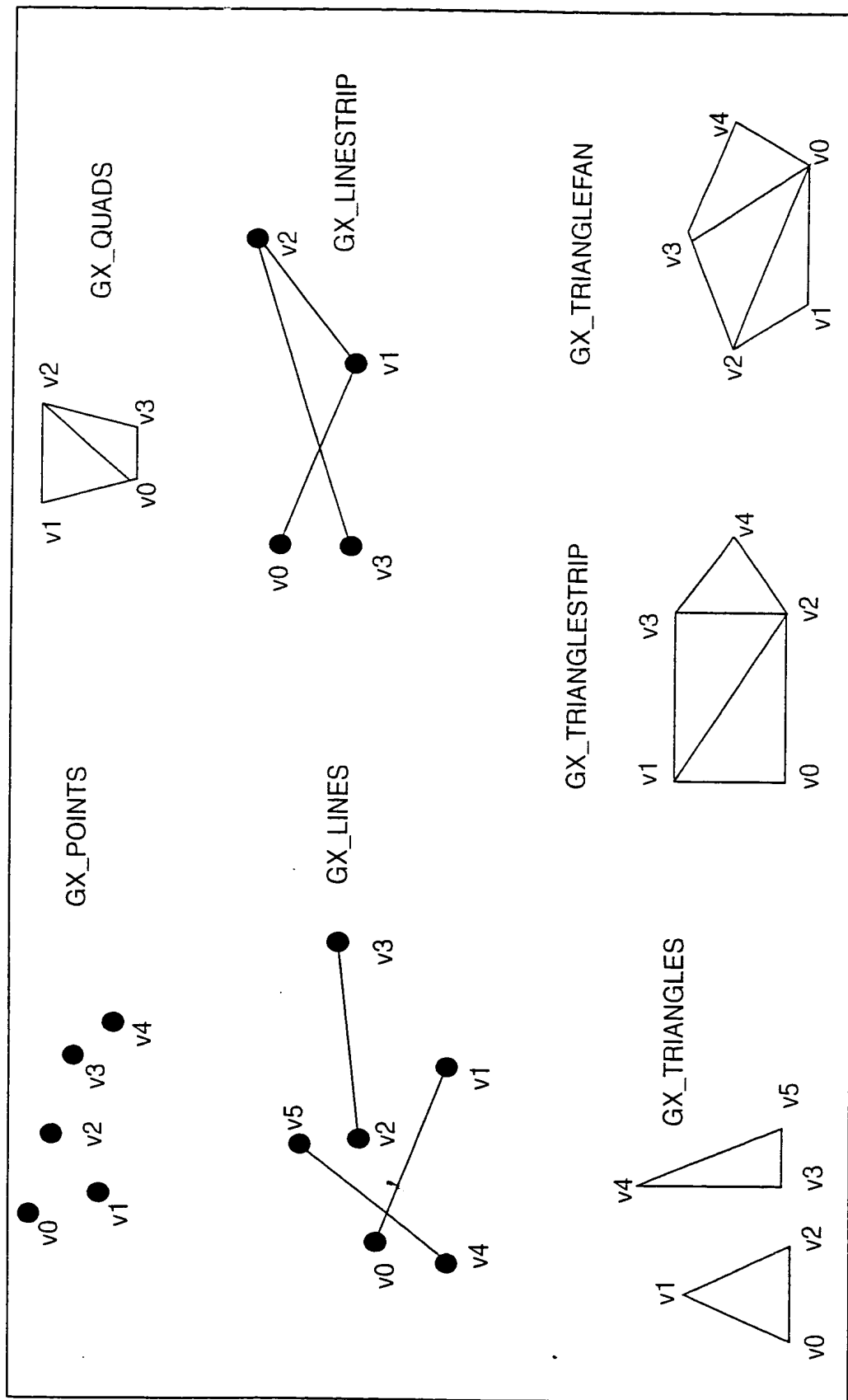
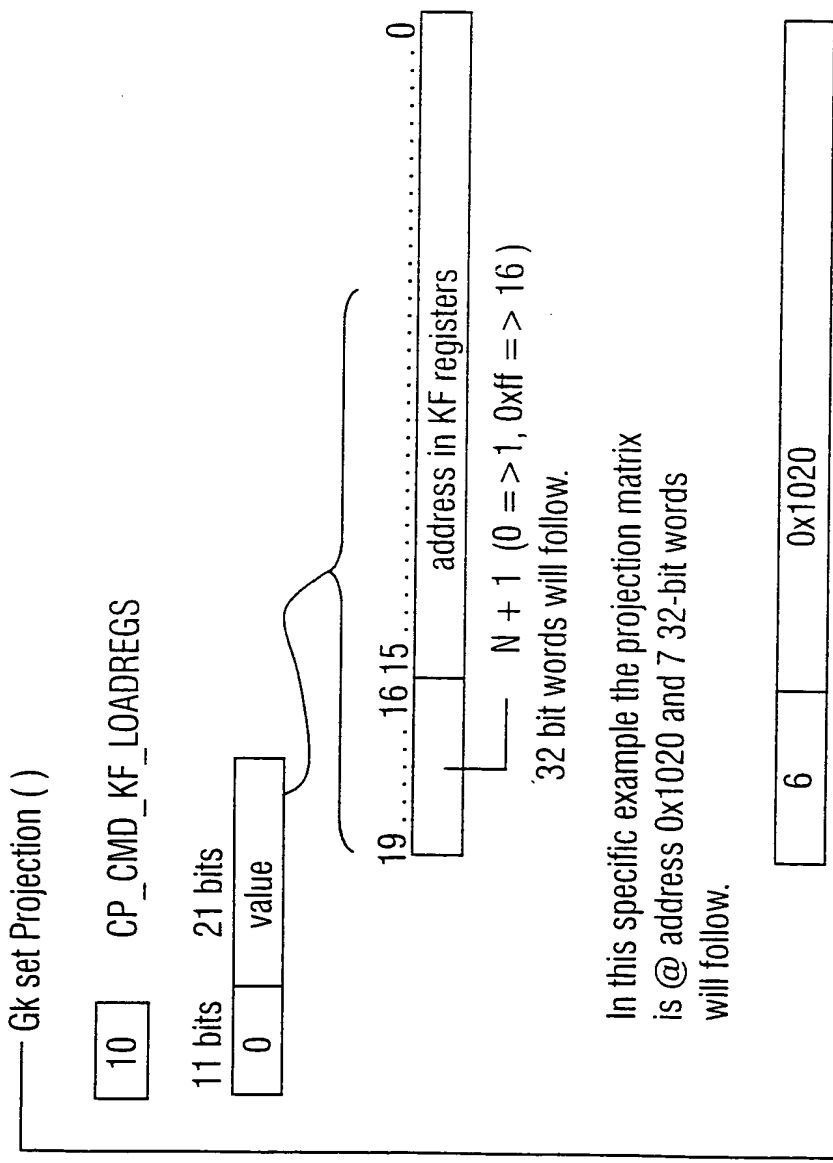


Fig. 15 EXAMPLE (GRAPHICS PRIMITIVES)



In this specific example the projection matrix is @ address 0x1020 and 7 32-bit words will follow.

Hence,

32-bits	
value	Proj A
value	Proj B
value	Proj C
value	Proj D
value	Proj E
value	Proj F
value	Proj G

Fig. 16

GX Call Display List ()

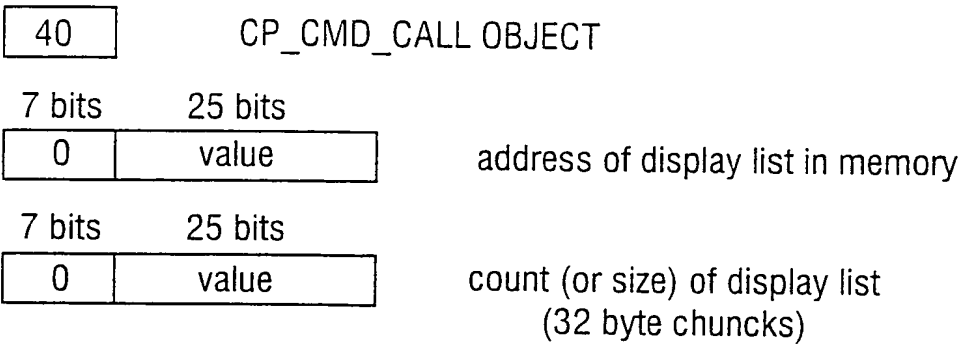


Fig. 17

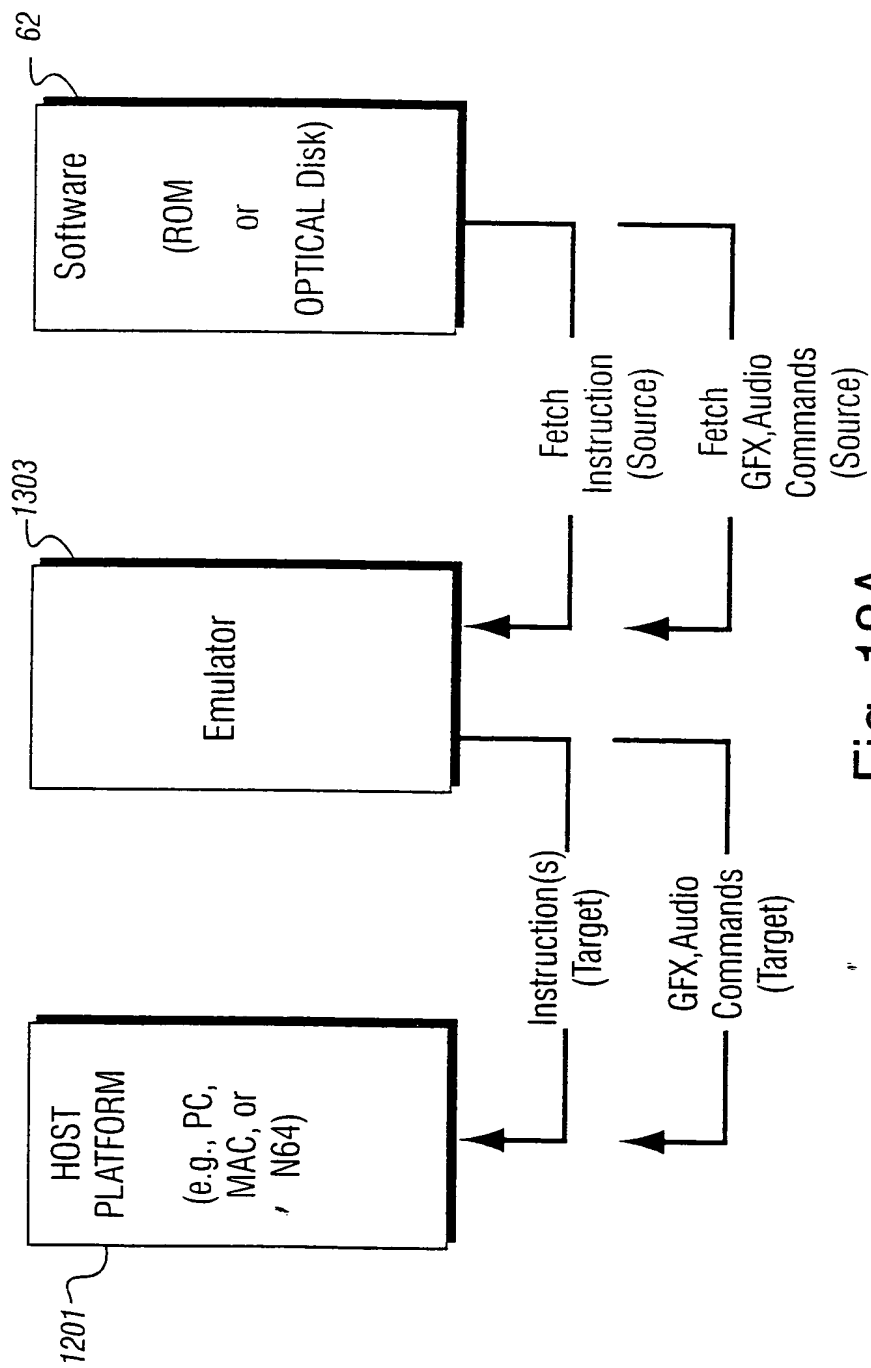


Fig. 18A

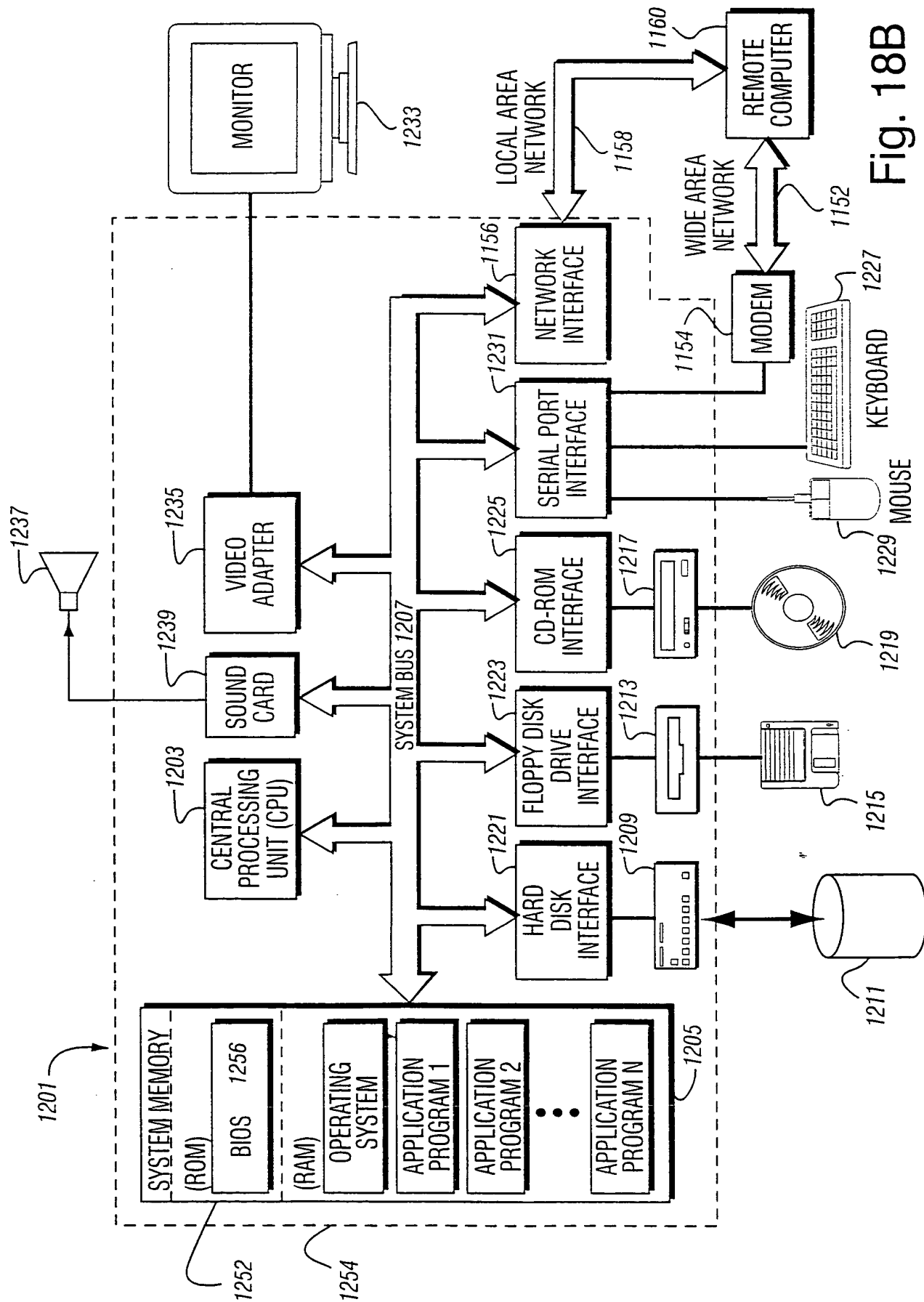


Fig. 18B